

**Regent Education and Research Foundation**  
**Department of EEE**  
**Routine for 2<sup>nd</sup> Internal Test (B.TECH)**

<b>B.Tech 2<sup>nd</sup> Year(EEE)</b>			
<b>Date: 16/11/2017</b>			
<b>Time:</b>	<b>10:30 – 11:00</b>	<b>11:00– 11:30</b>	<b>11:30 – 12:00</b>
<b>Subject:</b>	<b>M (CS) 301 Numerical Methods</b>	<b>EC(EE)301 Analog Electronic circuits</b>	<b>EC(EE)302 Digital Electronic circuit</b>
<b>Date: 17/11/2017</b>			
<b>Time:</b>	<b>10:30 – 11:00</b>	<b>11:00– 11:30</b>	<b>11:30 – 12:00</b>
<b>Subject:</b>	<b>M302 Mathematics-III</b>	<b>EE-301 Electric Circuit theory</b>	<b>EE-302 Field theory</b>
<b>B.Tech 3<sup>rd</sup> Year(EEE)</b>			
<b>Date: 16/11/2017</b>			
<b>Time:</b>	<b>1:00-1:30</b>	<b>1:30-2:00</b>	<b>2:00 – 2:30</b>
<b>Subject:</b>	<b>EEE-501 Electric machine- II</b>	<b>EEE-502 Power system-I</b>	<b>HU-501 Economics for Engineers</b>
<b>Date: 17/11/2017</b>			
<b>Time:</b>	<b>1:00-1:30</b>	<b>1:30-2:00</b>	<b>2:00 – 2:30</b>
<b>Subject:</b>	<b>EEE-503 Digital Signal Processing</b>	<b>EEE-504 C. Micro Processor &amp; Micro controller</b>	
<b>B.Tech 4<sup>th</sup> Year(EEE)</b>			
<b>Date: 16/11/2017</b>			
<b>Time:</b>	<b>3:00-3:30</b>	<b>3:30-4:00</b>	<b>4:00-4:30</b>
<b>Subject:</b>	<b>EEE-701 VLSI design</b>	<b>EEE-703A Electric drive</b>	<b>EEE-702 Digital Communication and network</b>
<b>Date: 17/11/2017</b>			
<b>Time:</b>	<b>3:00-3:30</b>	<b>3:30-4:00</b>	<b>4:00-4:30</b>
<b>Subject:</b>	<b>EE-704 C. Renewable &amp; Non conventional Energy</b>	<b>EEE-705 A. Operating system</b>	

**PRINCIPAL**