

**REGENT EDUCATION AND RESEARCH FOUNDATION
DEPARTMENT OF EE
LAB EXAM ROUTINE**

DATE	YEAR	10:00AM-1:00PM	2:00PM-5:00PM
22/11/2016	2ND	NUMERICAL LAB(GR-A)/ HU 381 (Gr B)	ANALOG &DIGITAL LAB(GR-A)/ CIRCUIT THEORY LAB(GR-B)
	3RD	POWER SYSTEM I LAB(GR-A)/ CONTROL SYSTEM I LAB(GR-B)	POWER SYSTEM I LAB(GR-B)/ CONTROL SYSTEM I LAB(GR-A)
	4TH	COMPUTER NETWORKS LAB(GR-A)	MACHINE DESIGN LAB(GR-A)
23/11/2016	2ND	NUMERICAL LAB(GR-B)/ HU 381 (Gr A)	ANALOG &DIGITAL LAB(GR-B)/ CIRCUIT THEORY LAB(GR-A)
	3RD	ELECTRICAL MACHINE LAB(GR-B)/ MICROPROCESSOR LAB(GR-A)	×
	4TH	ELECTRIC DRIVES LAB(GR-A)	ELECTRIC DRIVES LAB(GR-B)

24/11/2016	2ND	×	×
	3RD	SEMINAR EE-581	SEMINAR EE-581
	4TH	MACHINE DESIGN LAB(GR-B)	COMPUTER NETWORKS LAB(GR-B)
25/11/2016	2ND	×	×
	3RD	×	ELECTRICAL MACHINE LAB(GR-A)/ MICROPROCESSOR LAB(GR-B)
	4TH	PROJECT+SEMINAR ON INDUSTRIAL TRAINING(GR-A)	PROJECT+SEMINAR ON INDUSTRIAL TRAINING(GR-B)