

Regent Education and Research Foundation
Department of EE
Routine for 2nd Internal Test (B.TECH)

B.Tech 2nd Year(EE)			
Date: 16/11/2017			
Time:	10:30 – 11:00	11:00– 11:30	11:30 – 12:00
Subject:	M (CS) 301 Numerical Methods	EC(EE)301 Analog Electronic circuits	EC(EE)302 Digital Electronic circuit
Date: 17/11/2017			
Time:	10:30 – 11:00	11:00– 11:30	11:30 – 12:00
Subject:	M302 Mathematics-III	EE-301 Electric Circuit theory	EE-302 Field theory
B.Tech 3rd Year(EE)			
Date: 16/11/2017			
Time:	1:00-1:30	1:30-2:00	2:00 – 2:30
Subject:	EE-501 Electric machine- II	EE-502 Power system-I	HU-501 Economics for Engineers
Date: 17/11/2017			
Time:	1:00-1:30	1:30-2:00	2:00 – 2:30
Subject:	EE-503 Control system-I	EE-504 C. Micro Processor & Micro controller	
B.Tech 4th Year(EE)			
Date: 16/11/2017			
Time:	3:00-3:30	3:30-4:00	4:00-4:30
Subject:	EE-702 Utilization of Electric power	EE-701 Electric drive	EE-703 A. Power system-III
Date: 17/11/2017			
Time:	3:00-3:30	3:30-4:00	4:00-4:30
Subject:	EE-704 D. Renewable & Non conventional Energy	EE-705 A. Computer Network	

PRINCIPAL