

**REGENT EDUCATION AND RESEARCH FOUNDATION  
DEPARTMENT OF EEE  
LAB EXAM ROUTINE**

<b>DATE</b>	<b>YEAR</b>	<b>10:00AM-1:00PM</b>	<b>2:00PM-5:00PM</b>
<b>22/11/2016</b>	<b>2<sup>ND</sup></b>	<b>NUMERICAL LAB(GR-A)/ HU 381 (Gr B)</b>	<b>ANALOG &amp;DIGITAL LAB(GR-A)/ CIRCUIT THEORY LAB(GR-B)</b>
	<b>3<sup>RD</sup></b>	<b>DSP LAB(ALL)</b>	
	<b>4<sup>TH</sup></b>		<b>DISCOM LAB(ALL)</b>
<b>23/11/2016</b>	<b>2<sup>ND</sup></b>	<b>NUMERICAL LAB(GR-B)/ HU 381 (Gr A)</b>	<b>ANALOG &amp;DIGITAL LAB(GR-B)/ CIRCUIT THEORY LAB(GR-A)</b>
	<b>3<sup>RD</sup></b>	<b>ELECTRICAL MACHINE LAB(GR-B)/ MICROPROCESSOR LAB(GR-A)</b>	
	<b>4<sup>TH</sup></b>	<b>VLSI LAB(ALL)</b>	<b>OS LAB(ALL)</b>

<b>24/11/2016</b>	<b>2<sup>ND</sup></b>		
	<b>3<sup>RD</sup></b>	<b>SEMINAR</b>	<b>SEMINAR</b>
	<b>4<sup>TH</sup></b>	<b>MACHINE DESIGN LAB(ALL)</b>	
<b>25/11/2016</b>	<b>2<sup>ND</sup></b>		
	<b>3<sup>RD</sup></b>		<b>ELECTRICAL MACHINE LAB(GR-A)/ MICROPROCESSOR LAB(GR-B)</b>
	<b>4<sup>TH</sup></b>	<b>PROJECT+SEMINAR ON INDUSTRIAL TRAINING</b>	<b>PROJECT+SEMINAR ON INDUSTRIAL TRAINING</b>