

NOTICE

All EE/EEE B.Tech & Diploma students are informed that 2nd Internal test will be started from 17/11/2016. Routine is displayed on website.

Regent Education and Research Foundation Department of EE & EEE Routine for 2nd Internal Test (B.TECH)

B.Tech 2nd Year(EE & EEE)			
Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	EE-301	M(CS)301	EC(EE)301
Date: 19/11/2016			
Time:	10:30-11:30	12:00-1:00	1:30-2:30
Subject:	EC(EE)-302	EE302	M302
B.Tech 3rd Year(EE&EEE)			
Date: 17/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	EE502	EE 503/EEE-503	HU 501
Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	EE 501	EE 504C	
B.Tech 4th Year(EE)			
Date: 17/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	EE 703A	EE 701	EE 704D
Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	EE 702	EE 705A	
B.Tech 4th Year(EEE)			
Date: 17/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	DISCOM	ELECTRIC DRIVES	NON CONVENTIONAL

Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	VLSI	OS	

PRINCIPAL

Regent Education and Research Foundation
Department of EE
Routine for 2nd Internal Test (DIPLOMA)

2nd Year			
Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	Programming in C	Basic Electronics	Circuit Theory
Date: 19/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	Electrical Machine I	Electrical Measurement	Elements of Mechanical Engg.
3rd Year			
Date: 17/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	Switchgear & Protection	Electric Traction	Power Electronics
Date: 18/11/2016			
Time:	11.00 – 12.00	12:30 – 1.30	2.00 – 3.00
Subject:	Utilization	Microprocessor	

PRINCIPAL

NOTICE

**All 2nd year lateral entry students of EE & EEE are informed to attend the special lab of CIRCUIT THEORY & ANALOG & DIGITAL ELECTRONICS on Tuesday (15/11/2016) at 10.30 am
Contact with respective department.**